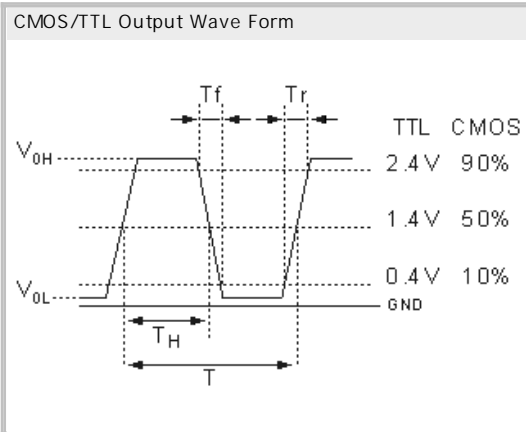
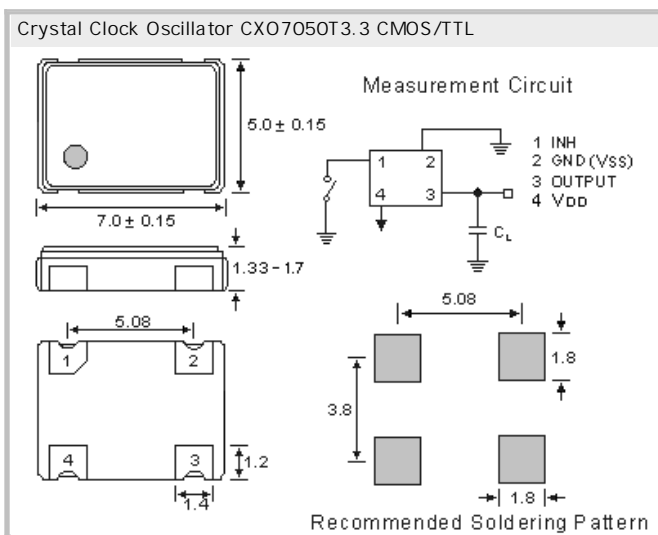


## Crystal Clock Oscillator CXO7050T3.3 3.3V CMOS/TTL, Low cost

- SMD in ceramic case (7.0 x 5.0 x 1.7) mm
- Tri-State Enable / Disable
- CMOS/TTL Square Wave
- on Tape & Reel (Tape 16mm)
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



## Specifications

|                                  |   |
|----------------------------------|---|
| Holder Type:                     | Crystal Clock Oscillator CXO7050T3.3 3.3V CMOS/TTL  |
| Frequency:                       | 3.579545 MHz  |
| Frequency Stability at 25°C:     | ± 50.0 ppm  |
| Operating Temperature Range:     | ± 50.0 ppm over -40°C to +85°C (inclusive of 25°C tolerance, ±10% input voltage variation, load change, aging, shock and vibration) |
| Storage Temperature:             | -50°C to +105°C   |
| Power Supply Voltage (Vdd):      | + 3.3V D.C. ± 5%  |
| Maximum Supply Current:          | 8.0 mA max.   |
| Output Load:                     | 15 pF CMOS / 10LS TTL gates max.  |
| Output Symmetry (Duty Cycle):    | 40/60%  |
| Output Voltage (VOH) (VOL):      | 90% of Vdd min. / 10% of Vdd max.   |
| Rise/Fall Time (10% to 90% Vdd): | 10 ns max. < 5 ns typical   |
| Start Up Time:                   | 10 ms max. < 5 ms typical   |
| Tri-state Function Pin 1:        | Pin 1 = H or open.... Output active at pin 3<br>Pin 1 = L.... high impedance at pin 3   |
| Phase Jitter (12 kHz to 20 MHz): | < 1pS RMS   |
| Aging:                           | < ± 5ppm max. / year (max.)   |
| Moisture sensitivity level:      | MSL= 1 per IPC/JEDEC J-STD-020 C  |
| Reflow Condition:                | 260°C max for 10 sec.   |

### GERMANY:

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